In the Drawings:

Please substitute the attached Figures 1, 2, 3, and 6 for the pending Figures 1, 2, 3, and 6. Marked-up copies of Figures 1, 2, 3, and 6 are also provided with the Request to Approve Proposed Drawing Corrections.

In the Abstract:

Please amend the abstract as follows (fully presented on a separate page):

SYSTEM AND METHOD FOR INDEPENDENT POWER SEQUENCING OF INTEGRATED CIRCUITS

ABSTRACT OF THE DISCLOSURE

A circuit for applying power to mixed mode integrated circuits in a predefined sequence. The circuit includes a first circuit powered by a first voltage and a second circuit powered by a second voltage that is less than the first voltage and having the second voltage coupled to the first circuit. The circuit for applying power to mixed mode integrated circuits includes[[,]] a modified [[IO]] I/O cell of the second circuit. The modified [[IO]] I/O cell has a driver transistor including a back gate terminal, a gate terminal that is driven by the second circuit, a ~~source~~ drain terminal that is coupled to a first circuit signal, and a ~~drain~~ source terminal that is coupled to the second ~~power supply~~ voltage. The circuit for applying power to mixed mode integrated circuits further includes[[,]] a controller circuit coupled to the first voltage and the second voltage supplied as controller circuit inputs. The controller circuit has a plurality of controller circuit outputs. The circuit for applying power to mixed mode integrated circuits also includes[[,]] a back gate bias application circuit. The back gate bias application circuit has a plurality of inputs coupled to the plurality of controller circuit outputs, and an output coupled to the ~~backgate~~ back gate of the driver transistor ~~backgate~~ back gate terminal.